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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/053,818	01/22/2002	Yinon Degani	SI	4719
7590 01/06/2004 Law Firm of Peter V.D. Wilde			EXAMINER NGUYEN, VINH P	
			2829	
		. •	DATE MAILED: 01/06/2004	· }

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)			
Office Action Summary	10/053,818	DEGANI ET AL.			
Office Action Summary	Examin r	Art Unit			
The MAILING DATE of this communication	VINH P NGUYEN	2829			
Period for Reply					
A SHORTENED STATUTORY PERIOD FOR REI THE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a - If NO period for reply is specified above, the maximum statutory peri - Failure to reply within the set or extended period for reply will, by state - Any reply received by the Office later than three months after the may earned patent term adjustment. See 37 CFR 1.704(b). Status	N. 1.136(a). In no event, however, ireply within the statutory minimum iod will apply and will expire SIX (itute, cause the application to become	may a reply be timely filed n of thirty (30) days will be considered timely. 6) MONTHS from the mailing date of this communication. ome ABANDONED (35 U.S.C. § 133).			
1) Responsive to communication(s) filed on 01	1 October 2003.				
2a)☐ This action is FINAL . 2b)⊠ The	nis action is non-final.				
	3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.				
Disposition of Claims					
 4) Claim(s) 1-19 is/are pending in the application. 4a) Of the above claim(s) 1-5,10-12,17 and 18 is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 6-913-16 and 19 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or election requirement. 					
Application Papers					
9)☐ The specification is objected to by the Exam	iner.				
10) The drawing(s) filed on is/are: a) a	accepted or b)□ objecto	ed to by the Examiner.			
Applicant may not request that any objection to t					
Replacement drawing sheet(s) including the con	•				
11) The oath or declaration is objected to by the	Examiner. Note the att	ached Office Action or form PTO-152.			
Priority under 35 U.S.C. §§ 119 and 120					
12) Acknowledgment is made of a claim for force a) All b) Some * c) None of: 1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the papplication from the International Bure * See the attached detailed Office action for a 13) Acknowledgment is made of a claim for domesince a specific reference was included in the 37 CFR 1.78. a) The translation of the foreign language 14) Acknowledgment is made of a claim for domesince as a claim for domesince	ents have been receive ents have been receive oriority documents have reau (PCT Rule 17.2(a)) list of the certified copie estic priority under 35 Ue first sentence of the sport provisional application estic priority under 35 Uestic priority under	d. d in Application No been received in this National Stage). es not received. J.S.C. § 119(e) (to a provisional application) becification or in an Application Data Sheet. has been received. J.S.C. §§ 120 and/or 121 since a specific			
Attachment(s) 1) Notice of References Cited (PTO-892)	4) [] Into	erview Summary (PTO-413) Paper No(s)			
2) Notice of Preferences Cited (PTO-692) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(5) 🔲 Not	tice of Informal Patent Application (PTO-152)			

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- 1. Applicant's election of species of figure 5 in Paper No. is acknowledged. Because applicant did not distinctly and specifically point out the supposed errors in the restriction requirement, the election has been treated as an election without traverse (MPEP § 818.03(a)).
- 2. It appears that claims 6-9,13-16 and 19 are read on elected species because the reference IC chip is not present on the elected species of figure 5.
- Claims 1-5,10-12,17-18 are withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected invention, there being no allowable generic or linking claim. Election was made without traverse in Paper No. 1003.
- 4. The title of the invention is not descriptive. A new title is required that is clearly indicative

of the invention to which the claims are directed.

1. Claims 6-9 and 14-16 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 6, it is unclear what "interconnection means" represents. Is it shown in any of drawings?

In claim 8, it is unclear what "part of a passive IC chip" is.

In claim 9, it is unclear what "part of a passive IC chip" is.

In claim 14, it is unclear how the step of matching impedance of the I/Os on the wafer with the reference component" is performed.

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The dependent claims not specifically address share the same indefiniteness as they depend from rejected base claims.

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claim 19 is rejected under 35 U.S.C. 102(b) as being anticipated by admitted prior art figure 2.

As to claim 19, admitted prior art figure 2 disclose a wafer under test (31) with a plurality of IC chips, a plurality of test contact (34) for making contact with the IC chips on the wafer and for performing a fully functional test of the IC chips. It is noted that the intended use "adapted for operation at frequencies above 1GHz" in the preamble is not given any patentable weight.

4. Claim 13 is rejected under 35 U.S.C. 102(b) as being anticipated by admitted prior art figure 2.

As to claim 13, admitted prior art figure 2 disclose a wafer under test (31) with a plurality of IC chips, a plurality of test contact (34) in a center portion of the probe membrane (18) for making contact with the IC chips on the wafer and for performing a fully functional test of the

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IC chips and a reference component (37,14) in a center portion. According to prior art figure 2, the center portion of the probe membrane is moved so that the probe contact array (34) comes into contact with the wafer under test.

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Evans et al (Pat # 4,975,638) disclose test probe assembly for testing integrated circuit devices.

Pasiecznik, Jr. (pat # 5,313,157) disclose probe for testing an electrical circuit chip.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to VINH P. NGUYEN whose telephone number is (703) 305-4914.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-4900.

VINH P. NGUYEN PRIMARY EXAMINER ART UNIT 2858

12/02/03